P. 01

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TO:	FROM:
Examiner Jonathan J. JOHNSON	Stephen T. Neal
COMPANY: USPTO	DATE: August 10, 2005
FAX NUMBER: (571) 273-8300	TOTAL NO. OF PAGES INCLUDING COVER: 5
PHONE NUMBER:	sender's reference number: Intel 2207/1266902
re: Serial No.: 10/612,804	YOUR REPERENCE NUMBER: Group Art Unit: 1725
☐ URGENT FOR REVIEW	☐ PLEASE COMMENT ☐ PLEASE REPLY ☐ CONFIRMATION
□ origi	NAL WILL FOLLOW ORIGINAL WILL NOT FOLLOW
Notes/Comments: AME	NDMENT AND RESPONSE
1. Fax Cover Sheet (1)	

2. Supplemental Amendment (4)

Total: (5) pages

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Patent

Attorney Docket No.: Intel 2207/1266902

Assignee: Intel Corporation

Supp. Response Under 37 C.F.R. §1.116

Expedited Procedure Examining Group 1725

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANTS

Terry STERRETT et al.

SERIAL NO.

10/612,804

FILED

7 1 4 000

FOR

July 1, 2003

OR :

A METHOD AND APPARATUS FOR IMPROVING

AN INTEGRATED CIRCUIT DEVICE

GROUP ART UNIT

1725

EXAMINER

Jonathan J. JOHNSON

VIA FACSIMILE

M/S: AF

Commissioner for Patents

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Dated: September 9, 2005

Signistore Thea Wagner

SUPPLEMENTAL AMENDMENT UNDER 37 C.F.R. § 1.116

Dear Sir:

Further to the Amendment filed August 10, 2005 in response to the Office Action dated June 10, 2005, Applicants submit the following amendments and remarks.

Amendments to the Claims are reflected in the listing of claims which begin on page 2 of this paper.

Remarks/Arguments begin on page 4 of this paper.

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